

REMARKS

Claims 1-6 are currently pending in the present application. In the Office Action of January 14, 2008, all of the claims were rejected under 35 U.S.C. § 102(b) as anticipated by Joy et al. (USP 4,454,647). In this Response, independent claim 1 has been amended to overcome the rejection. No new matter has been added by way of the amendments. Reconsideration and allowance of the claims in light of the amendments and arguments herein are respectfully requested.

Claim 1 has been amended to recite that the semiconductor component with trench isolation has a first covering insulation layer and a second covering insulation layer. The first covering insulation layer is below the surface of the semiconductor substrate and the second covering insulation layer is over the first covering insulation layer and above the surface of the semiconductor substrate. Support for this amendment is at least shown in Figure 1N (e.g., 10 is the first layer and 11 is the second layer) and discussed in the corresponding description.

Joy et al. discloses a single covering insulation layer 14 that is below the surface of the semi-conductor substrate. Joy et al. does not disclose a second covering insulation layer as recited in claim 1. Also, Joy et al. does not include a suggestion or provide a motivation for modifying the disclosed structure to include a second insulation layer. Thus, claim 1 and dependent claims 2-6 are believed novel and non-obvious over Joy et al. We respectfully request the allowance of claims 1-6.

CONCLUSION

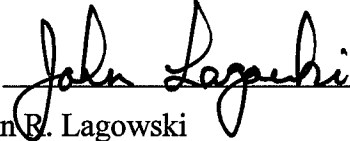
Therefore, in view of the above remarks, we respectfully submit that this application is in condition for allowance and such action is earnestly requested.

If for any reason the Examiner is not able to allow the application, he is requested to contact the Applicants' undersigned attorney at (312) 321-4200.

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Respectfully submitted,


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